IN THE CLAIMS

This listing of claims will replace all prior versions, and listings, of claims in the application. An identifier indicating the status of each claim is provided.

Listing of Claims

1. (Currently Amended) A digital signal processing apparatus, comprising:

a plurality of digital signal processing blocks including at least a signal processing block for decoding data streams, each of said plurality of digital signal processing blocks having a <u>processing unit</u>~~general-purpose Central Processing Unit~~;

a host processing block for controlling said digital processing apparatus by outputting a command of a high layer and not on a real time basis; and

a bus for connecting said host processing block and said plurality of digital signal processing blocks for transferring said command and for transferring said data of streams,

wherein said ~~Central Processing Units~~ <u>processing unit</u> of each of said digital signal processing blocks interprets and executes said command, and

wherein said <u>said</u> data <u>streams</u> may be assigned <u>high</u> priority <u>and transmitted at high speed.</u>~~in such a manner that streams of video data and streams of audio data that are assigned high priority are transmitted at high speed.~~

2. (Previously Presented) The digital signal processing apparatus as set forth in claim 1,

wherein said plurality of digital signal processing blocks include at least a front

end block for processing a received signal of a digital broadcast, and

wherein one of said plurality of digital signal processing blocks is a plug-in

interface block for connecting external hardware.

3 - 5.   (Canceled)

6. (Currently Amended) The digital signal processing apparatus as set forth in

claim 1,

wherein the command is described and embedded in a script of hypertext,

wherein the hypertext is interpreted by a browser and ~~a picture~~ an indication

for operating ~~the extension~~ a function is displayed, and

wherein a command corresponding to the function is generated. ~~embedded and~~

~~displayed in the picture for operating the extension function.~~

7. (Original)   The digital signal processing apparatus as set forth in claim 1,

wherein the data of streams contains video data and / or audio data.

8. (Original)   The digital signal processing apparatus as set forth in claim 7,

wherein the video data and / or the audio data has been compressed.

9. (Original)   The digital signal processing apparatus as set forth in claim 1,

wherein said bus is a general-purpose bus, and

wherein each block connected to said bus can be added or substituted.

10. (Original) The digital signal processing apparatus as set forth in claim 9,

wherein when each block connected to said bus is added or substituted,

software for operating the added or substituted block is automatically installed.

11. (Original) The digital signal processing apparatus as set forth in claim 9,

wherein software for operating the added or substituted block is stored in a

memory thereof, and

wherein when the block is added or substituted, the software stored in the

memory is installed.

12. (Original) The digital signal processing apparatus as set forth in claim 9,

wherein when each block connected to said bus is added or substituted, a

service center is accessed through a telephone line, software for operating the added or

substituted block is downloaded from the service center through the telephone line, and the

downloaded software is installed.

13. (Currently Amended) A digital signal processing method, comprising the

steps of:

structuring functions necessary for processing a digital signal as a plurality of

digital signal processing blocks and a host processing block, including at least a signal

processing block for decoding data ~~of~~ streams, each of said plurality of digital signal

processing blocks having a <u>processing unit</u> ~~general-purpose Central Processing Unit~~;

   connecting the host processing block and the plurality of digital signal

processing blocks through a bus; and

   outputting and transferring a command through the bus for controlling said

digital signal processing apparatus, said command being of a high layer and not on a real time

basis;

   wherein said ~~Central Processing Units~~ <u>processing unit</u> of each of said digital

signal processing blocks interprets and executes said command and outputs said data of

streams through the bus, and

   wherein <u>said</u> data <u>streams</u> may be assigned <u>high</u> priority <u>and transmitted at</u>

<u>high speed.</u>~~in such a manner that streams of video data and streams of audio data that are~~

~~assigned high priority are transmitted at high speed.~~

   14. (Previously Presented) The digital signal processing method as set forth in

claim 13,

   wherein the plurality of digital signal processing blocks include at least a front

end block for processing a received signal of a digital broadcast, and

   wherein one of said plurality of digital signal processing blocks is a plug-in

interface block for connecting external hardware.

   15 - 17. (Canceled)

18. (Original) The digital signal processing method as set forth in claim 13,

wherein the command is described and embedded in a script of hypertext.

19. (Original) The digital signal processing method as set forth in claim 13,

wherein the data of streams contains video data and / or audio data.

20. (Original) The digital signal processing method as set forth in claim 19,

wherein the video data and / or the audio data has been compressed.

21. (Original) The digital signal processing method as set forth in claim 13,

wherein the bus is a general-purpose bus, and

wherein each block connected to the bus can be added or substituted.

22. (Original) The digital signal processing method as set forth in claim 21,

wherein when each block connected to the bus is added or substituted, software

for operating the added or substituted block is automatically installed.

23. (Original) The digital signal processing method as set forth in claim 21,

wherein software for operating the added or substituted block is stored in a

memory thereof, and

wherein when the block is added or substituted, the software stored in the

memory is installed.

24. (Original) The digital signal processing method as set forth in claim 21,

wherein when each block connected to the bus is added or substituted, a

service center is accessed through a telephone line, software for operating the added or

substituted block is downloaded from the service center through the telephone line, and the

downloaded software is installed.

25. (New)   The digital signal processing apparatus as set forth in claim 1,

wherein said host processing block has a high level interface for processing

said command not depending on hardware structure; and

wherein said plurality of digital signal processing blocks has a driver for

interpreting said command, and a low level interface for controlling hardware.